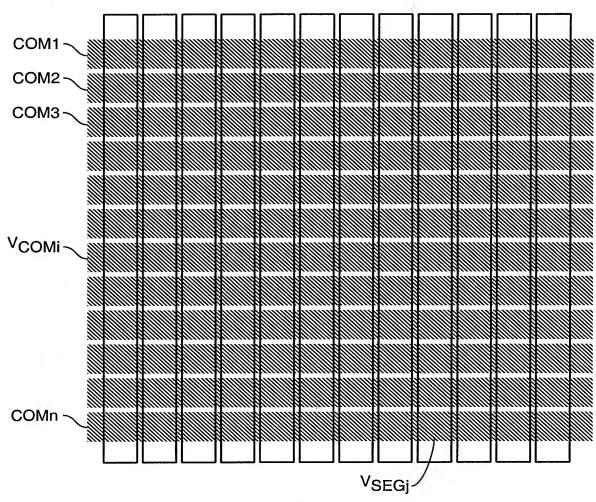
Appln. No. 10/665,810 VIA EFS Title: LCD Driver Inventors: Peter H. XIAO et al. Atty Docket: UTC1.007US1

Filing Date: 09/19/2003

REPLACEMENT SHEET

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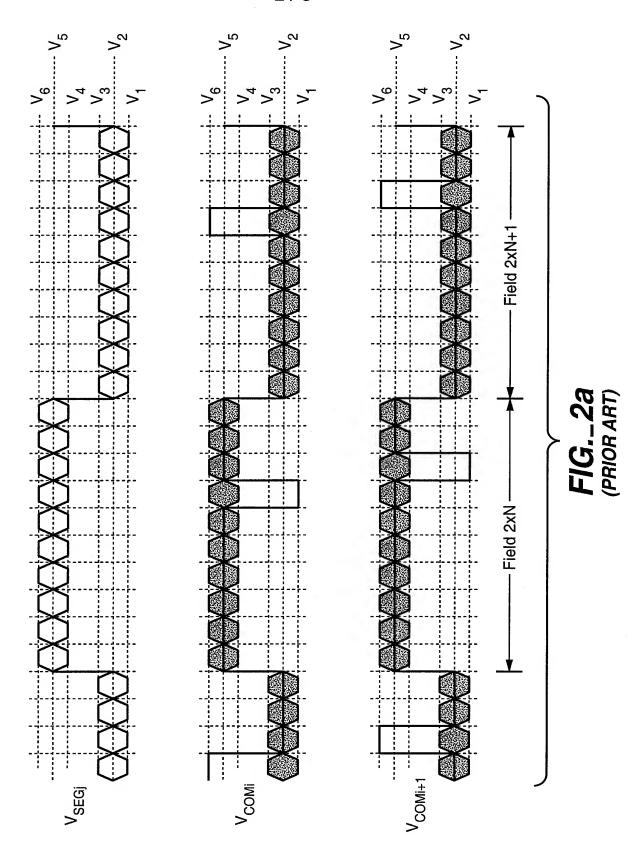
 $SEGI \sim SEGk$

FIG._1

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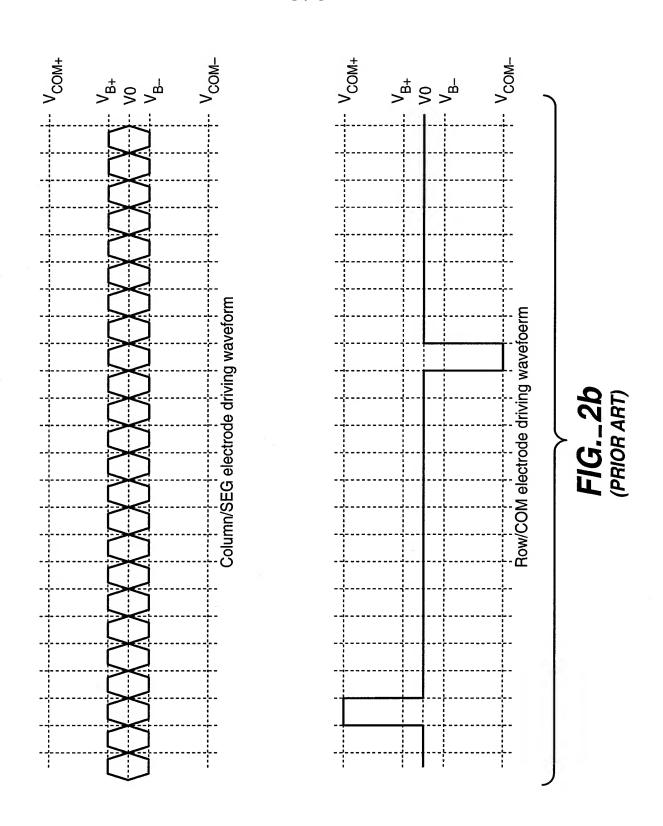


Appln. No. 10/665,810 VIA I Title: LCD Driver Inventors: Peter H. XIAO et al. Atty Docket: UTCI.007US1

VIA EFS

Filing Date: 09/19/2003

REPLACEMENT SHEET



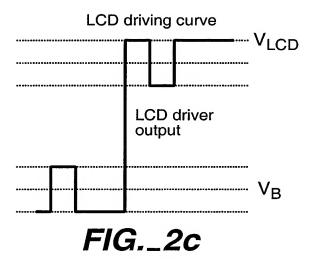
VIA EFS

Filing Date: 09/19/2003

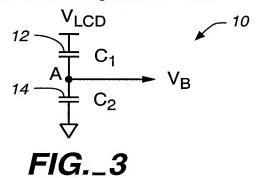
REPLACEMENT SHEET

Appln. No. 10/665,810 VIA E Title: LCD Driver Inventors: Peter H. XIAO et al. Atty Docket: UTCI.007US1

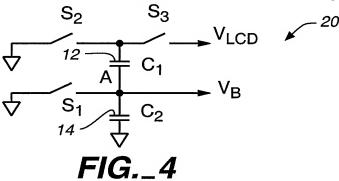
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A capacitor bias ratio generation circuit



A capacitor bias ratio circuit with periodic refreshing



VIA EFS

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Appln. No. 10/665,810 VIA E Title: LCD Driver Inventors: Peter H. XIAO et al. Atty Docket: UTCI.007US1

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An alternative capacitor divider bias ratio generation circuit

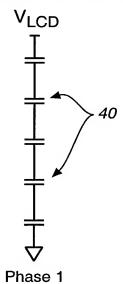


FIG._5a

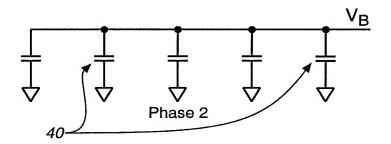


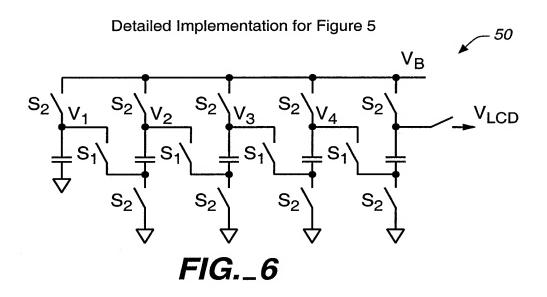
FIG._5b

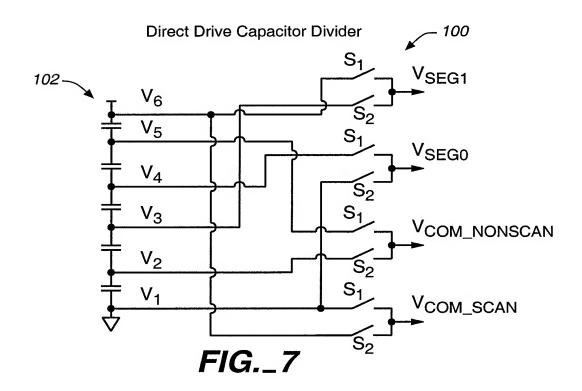
Appln. No. 10/665,810 VIA EFS Title: LCD Driver

Title: LCD Driver
Inventors: Peter H. XIAO et al.
Atty Docket: UTCI.007US1

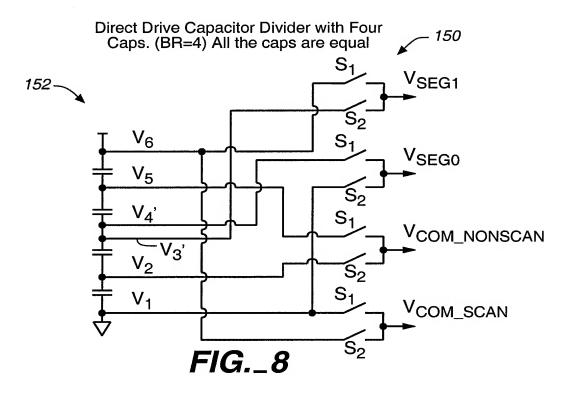
REPLACEMENT SHEET

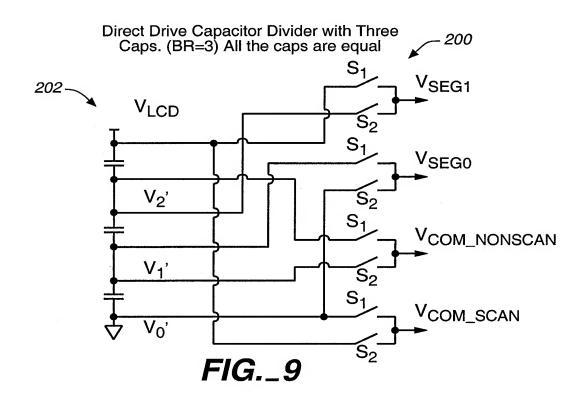
Filing Date: 09/19/2003





REPLACEMENT SHEET





Filing Date: 09/19/2003

REPLACEMENT SHEET

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		1/3 LCD driving waveforn	n
R1 R2 R3	C1	C2	
	V _{LCD} V ₂ ' V ₁ ' V ₀ '		 C ₁
		1-row scanning time	
	V_{LCD}		
	V ₂ '		. C ₂
	V ₁ '		–
	V ₀ '		•
	V _{LCD} V ₂ ', V ₁ ', V ₀ '		 R ₁
	V_{LCD}		
	V ₂ '		
	V ₁ '		R ₂
	V ₀ '		••
	V_{LCD}		
	V_2		
	V ₁ '		- R ₃

FIG._10